

PATENT NUMBER

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U.S. **UTILITY** Patent Application

<p>O.I.P.E.</p> <p>SCANNED <u>MD</u> G.A. <u>MT</u></p>	<p>PATENT DATE</p>
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APPLICATION NO. 09/733781	CONT/PRIOR F	CLASS 257	SUBCLASS 539	ART UNIT 2811 7215	EXAMINER W. Allen
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NTS
David H. Patti

High-voltage integrated vertical resistor and manufacturing process thereof

PTO-2040
12/99[illegible]

<input type="checkbox"/> TERMINAL DISCLAIMER	DRAWINGS		CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed. <input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S. Patent. No. _____ _____ _____	_____ (Assistant Examiner) (Date)		NOTICE OF ALLOWANCE MAILED	
	_____ (Primary Examiner) (Date)		ISSUE FEE	
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